

## ABSTRACT OF THE DISCLOSURE

The present invention relates to a gate drive device for a display. In the display, a timing control register is installed for  
5 rearranging the image signal data transmitted by a back end circuit of the display, and changing the open directions of the scan lines of each of the divided panel areas to the opposite directions by means of the image  
10 signal data transmitted to the display panel so as to resolve the problem of RC-delay, which makes the display image have uneven color in the jointing portion between the scan line gate drivers and makes the frame shift, caused by  
15 the excessive length of the scan line circuits of the display panel.